**Cover Letter**

Dear Editor,

Please find enclosed our manuscript entitled as *“SMT-Based Context-Bounded Model Checking for CUDA Programs”*, which we would like to submit for publication in the journal of Concurrency and Computation: Practice and Experience.

This paper extends our previous work published at WSCAD 2015 and SAC 2016. The version of ESBMC-GPU described and evaluated here has been optimized and extended. It now expands our operational model to support more CUDA libraries (*e.g.*, math functions) and includes new SMT solvers as back-end for ESBMC-GPU. We have also improved our operational model, the application of MPOR to CUDA programs, and the two-thread analysis. We have also significantly expanded the experimental basis, including a new state-of-the-art GPU verifier (*i.e.*, CIVL), and evaluate ESBMC-GPU with (respectively, compare it against) the most recent stable versions of the GPU verifiers.

For your reference, we attach the original papers (wscad2015.pdf and sac2016.pdf) with this submission. These new results have not been published elsewhere and are not under consideration by another journal.

Thank your for your time.

Sincerely Yours,

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